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DIGITRANS

Sharing Remote Experiment Environment (SREE)

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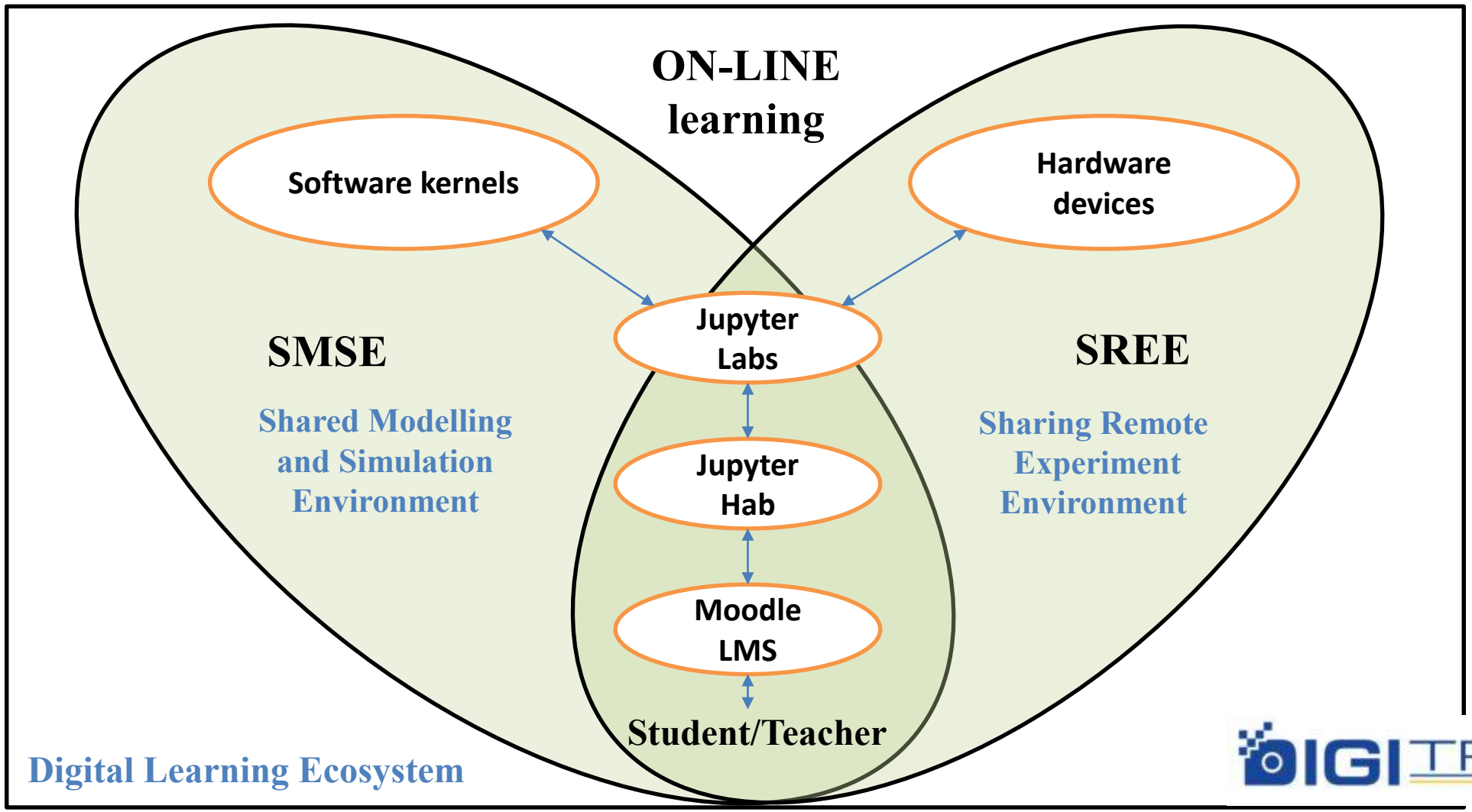
Digital Learning Ecosystem (DLE)

Co-funded by the Erasmus+ Programme of the European Union



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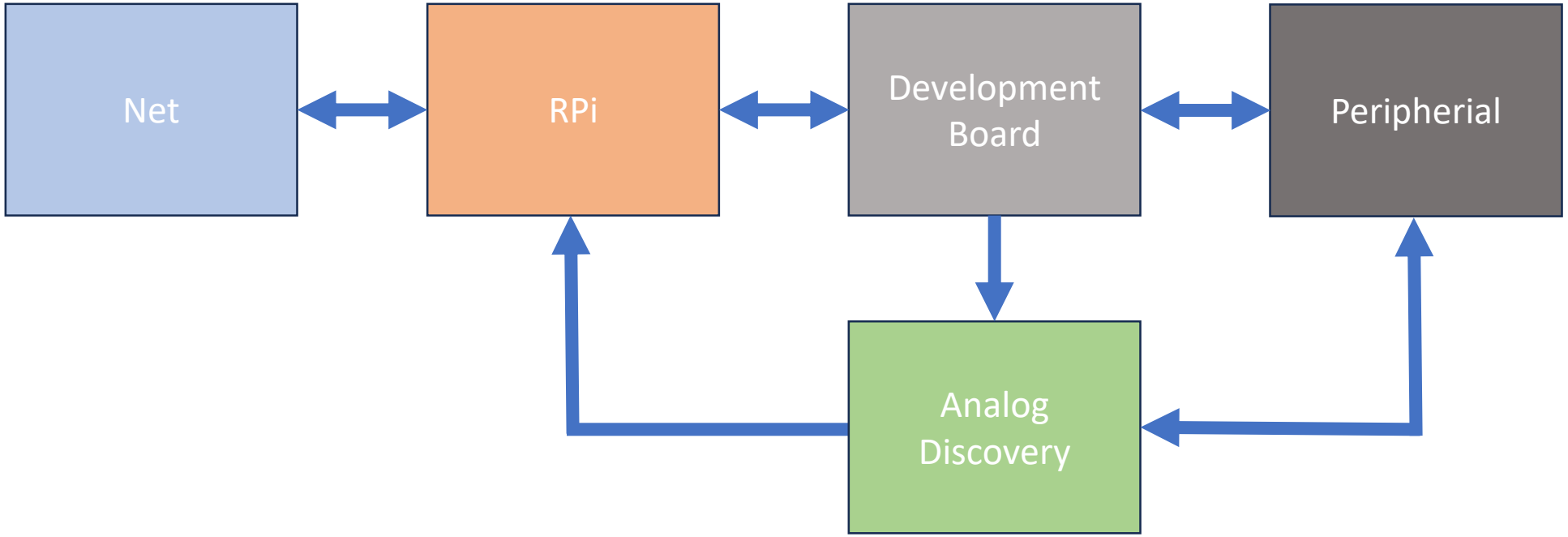
Structure of SREE

Co-funded by the Erasmus+ Programme of the European Union



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Embedded & FPGA remote labs

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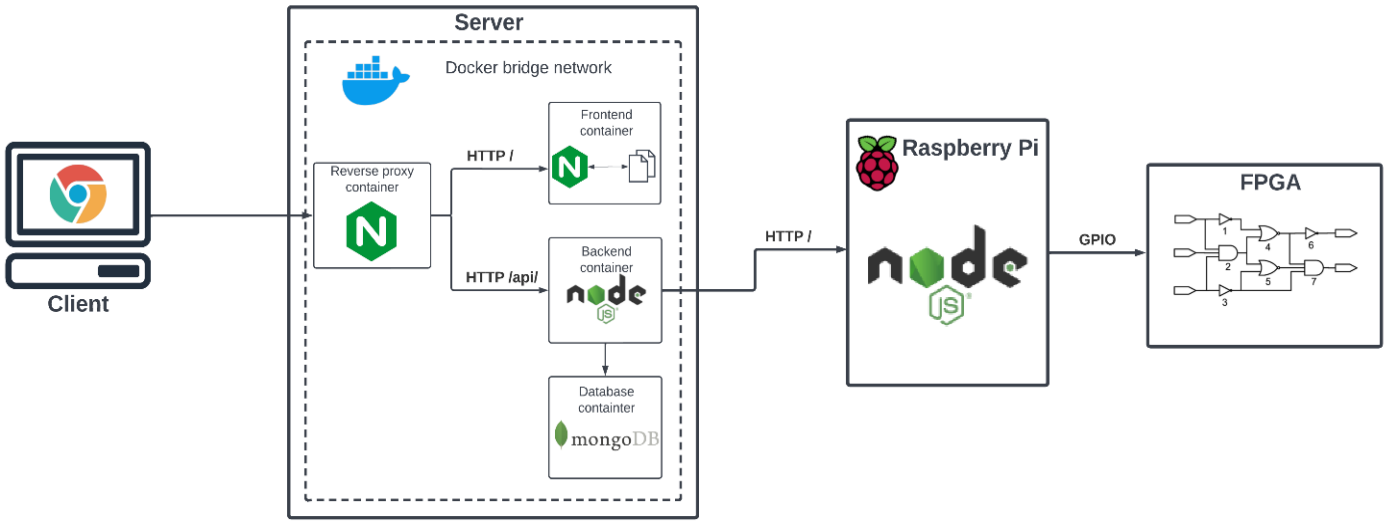

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Microcontroller development board

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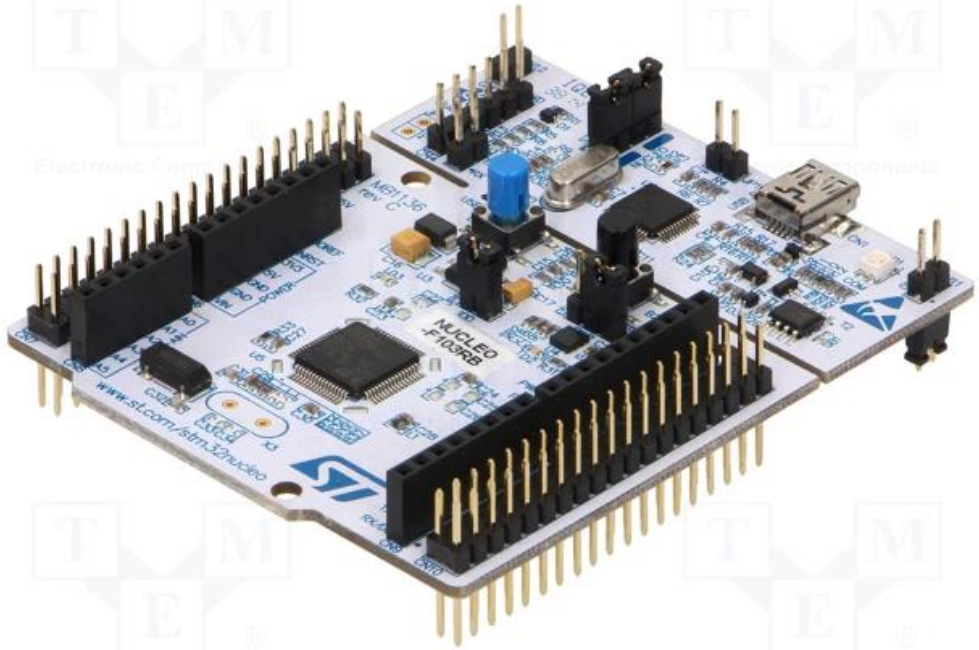
NUCLEO-F103RB

ST Microelectronics



STM32F3DISCOVERY

ST Microelectronics

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FPGA board

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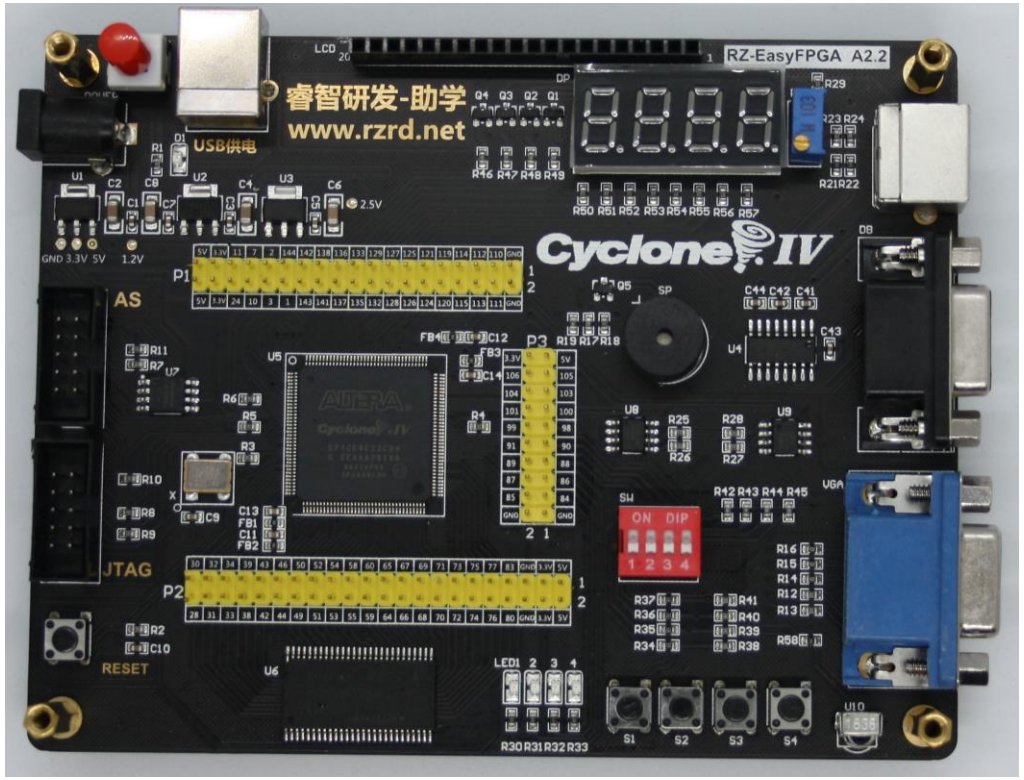
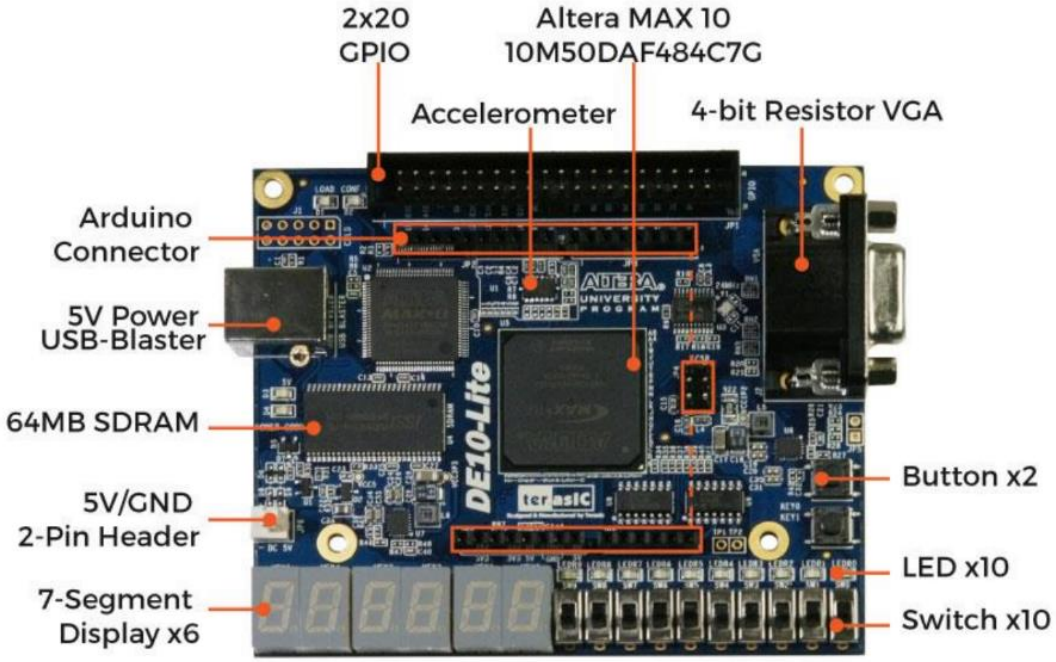
Intel/Altera FPGA - Cyclone IV: OMDAZZ, RZRD



Intel/Altera FPGA - MAX 10: Terasic DE10-Lite



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Laboratory hardware construction for training

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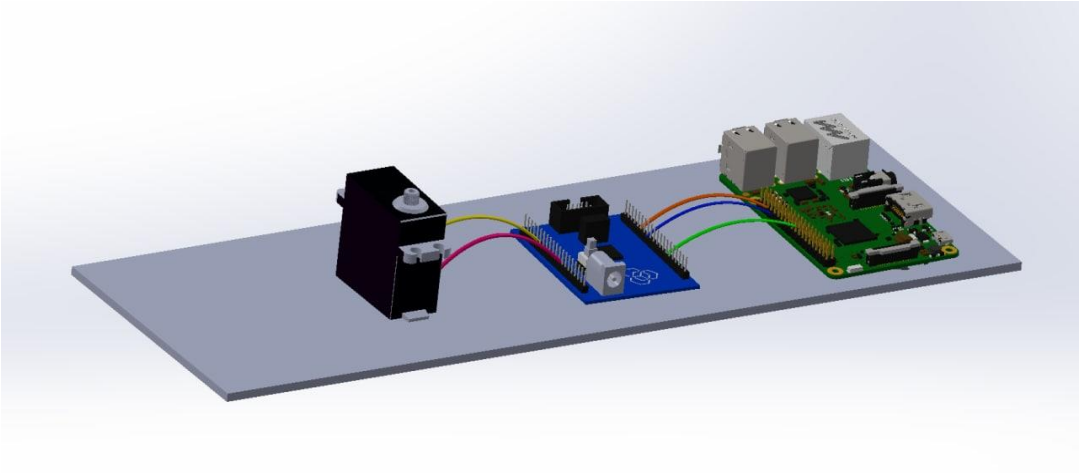

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Motor Driver Controller Board

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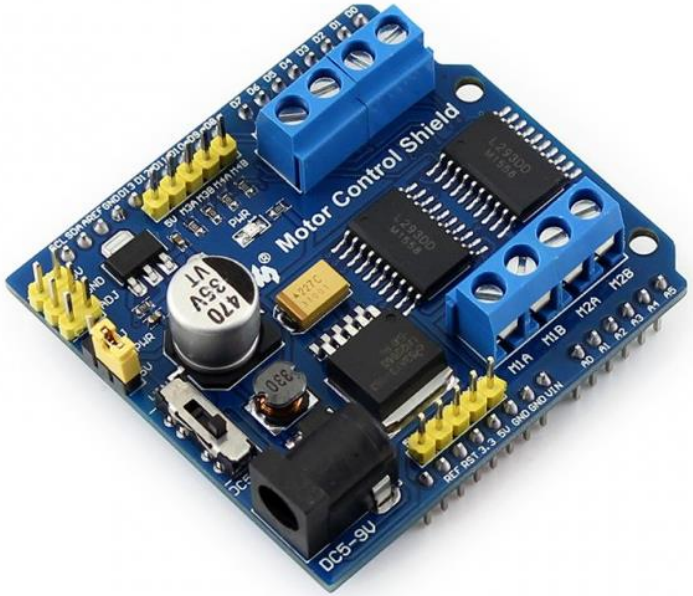
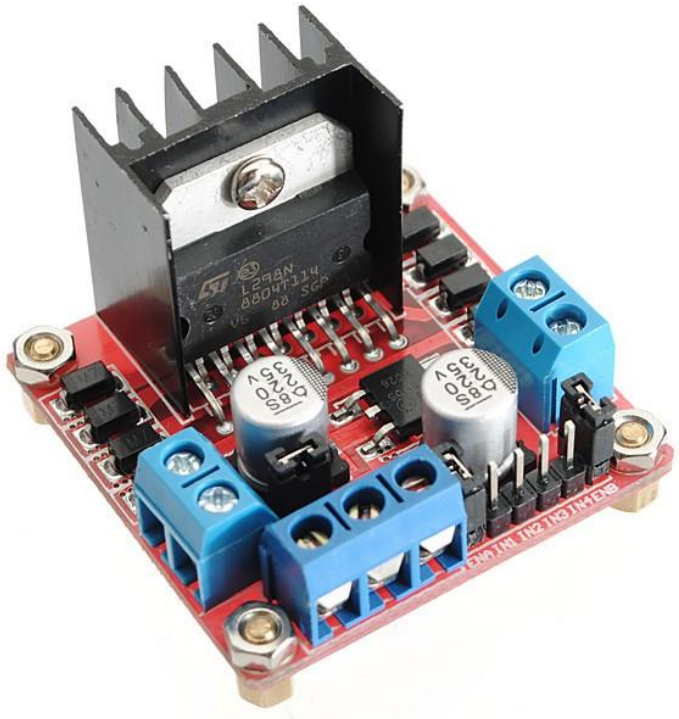
L293D DC Motor Driver Module



L298N Motor Driver Controller Board



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Experiment with Power Converter remote lab

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